

# **Programming Note**

3326A/0000-2 SEPTEMBER 1984

Supersedes: None

## **Quick Reference Guide**

for the HP 3326A Two-Channel Synthesizer



#### INTRODUCTION

This programming note is a reference guide for the remote operation of the HP 3326A Two-Channel Synthesizer. This note is intended for use by those familiar with HP-IB programming and the basic functions of the HP 3326A Two-Channel Synthesizer. For complete programming information refer to the HP 3326A Operating Manual.

## INPUT DATA

The 3326A Two-Channel Synthesizer accepts programming codes that contain information for programming all of the front panel and special HP-IB only functions except the Line switch and Set HP-IB Address. The programming data

string consists of a string of ASCII coded characters composed of one or more of the following control fields:

- Channel Select
- Entry Select
- Function Select
- Sweep Mode
- Mode Select
- Modulation
- Instrument State/Registers
- Calibration
- Special HP-IB Only Functions

**Input Syntax.** The 3326A responds to program codes in the order in which they are received. Each event or performed action is programmed with a string of ASCII coded characters that follow one of the following sequences.



- Numeric Entry: [Entry Prefix] [Numeric Value] [Numeric terminator]
- Select Value/Mode: [Entry Prefix] [Numeric Value]
- Immediate Action: [Action Code]

#### NOTE

The HP-IB program code sequence typically mirrors that of the local front panel keystroke sequence, except the shift functions which have special HP-IB codes.

**Entry Prefix/Action Codes.** Entry prefix/action codes are typically 2 to 5 character mnemonics. For an entry that has a numeric value associated with it, programming the entry prefix code only will enable and activate the numeric display of the current value.

**Numeric Values/Formats.** These are either a single decimal digit, a set of 14 characters or less representing a number, or a string of binary bytes. A string of 15 characters maximum can be expressed in exponential, decimal, or integer form. Acceptable numeric formats are referenced in later sections by the following format syntax:

Format #1: Exponential	$\pm d***d.d***dE \pm dd$
Format #2: Decimal	± d***d.d***d
Format #3: Integer	± d***d
Format #4: Single Digit	d
Format #5: Binary String	b***b

The character 'd' indicates a leading or trailing zero, a space, or a numeric digit (0 through 9). The character 'b' indicates an 8-bit binary byte. The characters '\*\*\*' indicate a variable number of the previous character. Numeric values are scaled by the appropriate numeric terminator. Negative signs are ignored and the positive value used if the parameter can not have negative values.

**Numeric Terminators.** Numeric terminators are multicharacter codes that terminate and scale the associated numeric value. Thus, frequency values can be entered in MHz (MHZ), kHz (KHZ), or Hz (HZ); sweep time values can be entered in seconds (SEC) or milliseconds (MS); amplitude values can be entered in dBm (DBM), dBV (DBV), Volts peak-to-peak (VO), Volts RMS (VRMS), millivolts peak-to-peak (MV), or millivolts RMS (MR); phase in degrees (DEG); and modultation level in % AM (PC) or degrees PM (DEG).

**Valid Characters.** The valid characters in program codes can be either upper or lower case characters (A-Z, a-z) since they can be interchanged, digits (0-9), decimal point (.), plus/minus signs (+, -), question mark (?), and pound sign (#). The parity bit (ie. 8th bit) is ignored by the 3326A.

**Delimiters.** All alpha programming codes must be delimited by a non-valid character (ie. space, comma, semicolon) for that action to be activated and the next event processed. If a valid character is used, a syntax error will occur.

**Programming Data.** The 3326A buffers up to 3 or 100 characters of HP-IB data, depending on the bus mode selected. Thus care must be taken to ensure a desired action has occurred. This can be accomplished via the WAIT command or a serial poll of the status byte ready bit.

Table 1 lists all Input Programming Codes and their syntax.

**Instrument Preset.** Instrument Preset turns off all functions then sets the following:

	Channel A:	
	Frequency	1000 Hz
	Amplitude	100 mV pp
	DC Offset	0 V
	Phase	0 deg
	Function	sine
	Start Frequency	0 Hz
	Stop Frequency	13 MHz
•	Channel B:	10 1111 12
	Frequency	1000 Hz
	Amplitude	100 mV pp
	DC Offset	0 V
	Phase	0 deg
	Function	sine
	Start Frequency	0 Hz
	Stop Frequency	13 MHz
•	Mode	Two Channel
•	Duty Cycle	50%
•	Modulation Level	30% (AM)
•	Marker Frequency	6.5 MHz
•	Marker Channel	Channel A
•	Sweep Time	1 sec
•	Sweep Mode	Ramp
•	Calibration Mode	Internal
•	Channel Selected	A
•	Trigger Action Pending	Single Sweep

Instrument Preset does not affect the Storage Registers, HP-IB address, or Service Request Mask value.

## **OUTPUT DATA**

The 3326A has several output modes that allow the user to learn and interrogate the present instrument state. The following output modes are available:

- Learn String
- Interrogate Parameter
- Interrogate Error

All messages are terminated by asserting the bus EOI signal in parallel with the last byte of the message to be sent.

Learn String: Selected with the "LRN" program code, the 3326A outputs a Learn String of 172 bytes in length. This binary data string completely describes the present instrument state saved in the specified Storage Register of the 3326A. The information is packed and encoded for minimal storage requirements thereby making data analysis difficult. When stored in an ASCII character data string, the Learn String can later be input to any Storage Register of the 3326A to save that instrument state (see Table 1 for Learn String information). The length of the Learn String is fixed, independent of the functions selected.

Format: 172 [8 bit bytes] [EOI]

Interrogate Parameter: Selected with the "I" preceding the program code for the parameter to be interrogated, or a "?" following the program code, the 3326A will output an ASCII string composed of the parameter code, present numeric value, and units. The numeric value indicates either the present status, mode or value. Values are expressed in fundamental units, ie. Hz, seconds, volts, degrees, and %. Table 1 also lists the output format for each valid parameter to interrogate.

Format: [program code] [numeric value] [units] [CR] [LF] [EOI]

**Interrogate Error:** Selected with the "ERR?" program code, the 3326A outputs a numeric value corresponding to the most recent error number. Table 3 lists the possible error numbers and their causes.

Format: [numeric value] [CR] [LF] [EOI]

#### **TRIGGER**

The 3326A responds to HP-IB Commands Group Execute Trigger (GET) and Selective Device Trigger (SDT) depending upon the last trigger action command. Receipt of either command causes the 3326A to perform an action specified by the Trigger Action command.

## CLEAR

The 3326A responds to both Device Clear (DCL) and Selective Device Clear (SDC) by clearing all bits of the status byte then setting bit 4 (Ready for Data), clearing the HP-IB command buffer, and initializing the interface so that it is ready to receive HP-IB programming codes. This is necessary if the instrument state prior to sending HP-IB commands is unknown. It is good practice to execute DCL or SDC at the beginning of any program.

## REMOTE/LOCAL CHANGES

The 3326A goes to the Remote state when the LREN line is true (low) and the 3326A receives its listen address. In Remote, all front panel functions are disabled except the LINE switch and the LOCAL key. The LOCAL function can also be disabled via the Local Lockout (LLO) command.

The 3326A goes to the Local state when it receives the Go To Local (GTL) command or when the LREN line is set false (high). If the Local Lockout (LLO) command has not been executed, the 3326A can also be set to Local by pressing the LOCAL key. In Local, the front panel is active but the instrument will still respond to HP-IB programming codes.

#### SERVICE REQUEST

The 3326A can initiate a Service Request (SRQ) whenever one of the following conditions exists:

- Programming error (syntax, incompatible mode, etc.)
- Sweep in progress
- Hardware error
- Ready for data
- Power Failure/On

Further information can be obtained by conducting a Serial Poll, which accesses the Status Byte. The SRQ is cleared only by executing a Serial Poll. To select an SRQ for a particular set of circumstances, the Request Mask function can be used to determine which of the bits in the Status Byte can cause an SRQ. The mask value is determined by summing the decimal values of each selected function/condition that is desired. The default Request Mask at power on is "00000000" or decimal 0. The mask value is reset to the default value only at power on or by the front panel memory clear function.

## STATUS BYTE

The 3326A responds to a Serial Poll by sending its status byte as indicated in Table 2. When Bit 6 (Request Service) of

TABLE 1. HP3326A HP-IB MNEMONIC SUMMARY

	Front Panel Control	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
CALIBRATION BLOCK						
beock	AUTO	ACAL or	0-1	-	_	AutoCALibration Syntax: "ACAL0"
		ACAL		OFF, ON		"ACAL OFF"
	MANUAL	CAL		_	-	CALibrate Syntax: "CAL"
	SELECT	CMD or	1	_	-	Calibration MoDe - INTernal
		CMD	ion surá	INT		Syntax: "CMD1" "CMD INT"
		CMD or	2	_		Calibration MoDe - EXTernal
		CMD	7	EXT		Syntax: "CMD1" "CMD EXT"
		CMD or	3	_		Calibration MoDe - MULTiphase
		CMD	<del>-</del>	MULT		Syntax: "CMD3" "CMD MULT"
	SELF TEST	TST		_	###########	self TeST, each # = P or F for Pass or Fail Syntax: "TST"
ENTRY BLOCK				<del>, ,</del> ,		
	AMPTD	AM	0-10 V	VO, VRMS, DBM, DBV	AM ± #.###E ± ##VO	AMplitude Resolution: 1 mV p-p Syntax: "AM1.125VRMS"
	ASGN ZERO φ	ZPH		-		Zero PHase Syntax: "ZPH"
	CLR $\phi$ OFS	COF		-		Clear phase OFfset Syntax: "COF"
	DC OFFSET	OF	±5 V	VO	OF ±#.###E±##VO	OFfset Resolution: 10 mV Syntax: "OF3.02VO"
	FREQ	FR	0-13 MHz	HZ, KHZ, MHZ	FR #########HZ or FR #########HZ	FRequency Resolution: 1 µHz f<100 kHz, 1 mHz f≥100 kHz Syntax: "FR7.5MHZ"
	PHASE	PH	±720°	DEG	PH ±#.####E±##DEG	PHase Resolution: 0.01° Syntax: "PH180.05DEG"
	DUTY CYCLE	DUTY	1-99%	PC	DUTY#.####E ± ##PC	DUTY cycle Resolution: 0.01% Syntax: "DUTY25.50PC"
	% AM/	ML	0-100%	PC	ML ±#.##E±##PC	Modulation Level
	PM DEV	or ML	0-360°	DEG	or ML ±#.###E±##DEG	Resolution: 0.1% or 1° Syntax: "ML30.5PC"

	Front Panel Control	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
FUNCTION			en (1.19)			
	CHA	FCNA or	0	_	_	FunCtioN channel A OFF
		FCNA		OFF		Syntax: "FCNA0" "FCNA OFF"
		FCNA or	1		_	FunCtioN channel A
		FCNA	-	SIN		Syntax: "FCNA1" "FCNA SIN"
		FCNA	2		_	FunCtioN channel A
		or FCNA	_	SQR		SQuaRe Syntax: ''FCNA2'' ''FCNA SQR''
		FCNA	3		_	FunCtioN channel A D Syntax: "FCNA3"
		or FCNA	-	DC		"FCNA DC"
	CH A HV	HVA or	0-1	_	_	High Voltage channel A Syntax: "HVA1"
	TIV	HVA	_	OFF, ON		"HVA ON"
	СНВ	FCNB or	0	<u></u>	_	FunCtioN channel B
		FCNB	_	OFF		Syntax: "FCNB0" "FCNB OFF"
		FCNB or	1		_	FunCtioN channel B SINe
		FCNB	<del>-</del>	SIN		Syntax: "FCNB1" "FCNB SIN"
		FCNB	2	_		FunCtioN channel B
		or FCNB	_	SQR		SQuaRe Syntax: "FCNB2" "FCNB SQR"
		FCNB or	3			FunCtioN channel B D Syntax: "FCNB3"
		FCNB	_	DC		"FCNB DC"
	CH B HV	HVB or	0-1	-		High Voltage channel I Syntax: "HVB1"
		HVB	_	OFF, ON		"HVB ON"
BUS MODES		DUIONA	4.0		·	DUO Mada
		BUSM	1-2			BUS Mode Syntax: "BUSM2" no operation
		VVAII		_		Syntax: "WAIT"
		<u></u>	٠.			
DISPLAY CONTROL						
		DISP or	0-1	_		DISPlay control Syntax: "DISP1"
		DISP		OFF, ON		"DISP ON"

Front Panel Control	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
ERROR CODES	ERR?	_	_	ERR ###	ERRor code Syntax: "ERR?"
HP 3326A IDENTIFICATION	ID?	_	-	HP3326A	IDentification Syntax: "ID?"
	RDY?	_	-	0	ReaDY Syntax: "RDY?"
	REV?	-	-	####,####	REVision Syntax: "REV?"
	SER?	_	-	####A00000	SERial number Syntax: "SER?"
MODIFYING PARAMETERS	DN	_			DowN increment by EINC value Syntax: "DN"
	EINC	see des	cription		Entry INCrement for UP, DN, TUP, and TDN commands Use increment resolution and suffix appropriate for entry value modified Syntax: "EINC1HZ" "EINC.1VRMS"
	UP	_	-	_	UP increment by EINC value Syntax: "UP"
READING AND MASKING THE STATUS BYTE					
	MASK	0-255	PC	MASK###PC	SRQ MASK (weighted binary sum of bit positions) Syntax: "MASK32PC"
SAVING OR RESTORING AN HP 3326A					
SETUP	LRN	0-9	_	_	LeaRN (read) nonvolatile memory Syntax: "LRN3"
	PRG	0-9		_	ProGram (restore) non- volatile memory Syntax: "PRG3"

	Fror Pan Con	el	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
TRIGGERED							
OPERATION		STC	<u>-</u>	<del>-</del>	_	_	Sweep Triggered Continuous Syntax: "STC"
		STS	-	_	_	-	Sweep Triggered Single Syntax: "STS"
		TDN	_	_	_	4.1094	Trigger DowN increment by EINC amount Syntax: "TDN"
		TOFF	-		_		Trigger OFF Syntax: "TOFF"
		TUP	ŶŢ.			-	Trigger UP increment by EINC amount Syntax: "TUP"
INSTR STATE	THE REAL PROPERTY.						
BLOCK	INSTR PRESET	RST	÷ ;-	_		<del>-</del>	ReSeT Syntax: ''RST''
	RCL DISCRETE	DRCL	. 00-62		_	-	Discrete ReCaLI element Syntax: "DRCL02"
	RECALL	RCL	0-9	- 1 te -	<u> </u>		ReCaLI memory Syntax: "RCL3"
	RST DISCRETE	CLR	<del>у Т</del>		_	_	Discrete sweep CLeal elements Syntax: "CLR"
	SAVE	SAV	0-9	_	_	- 1	SAVe memory Syntax: "SAV3"
	SAVE DISCRETE	DSAV	00-62		_		Discrete SAVe elemer Syntax: ''DSAV02''
MODE BLOCK	COMBINED	CMB or CMB	0-1	— OFF,		<u></u>	CoMBiner Syntax: "CMB1" "CMB ON"
				011,			
	MODE	or MODE		TWO	_ C	7	MODE TWO Channel Syntax: "MODE1" "MODE TWOC
		MODE or		_			MODE TWO Phase Syntax: "MODE2"
		MODI	E –	TWO	P		"MODE TWOP"
		MODI or	E 3	_	_	-	MODE TWO Tone Syntax: "MODE3"
		MODI	E —	TWO	Т		"MODE TWOT"
		MODI	E 4	_	<b>#</b> _	4_03	MODE PULSe Syntax: "MODE4"
		MODI	E –	PULS	3		"MODE PULS"

	Front Panel Control	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
MODIFY BLOCK	ON/OFF	MFY	0-1			front panel ModiFY
	ON/OFF	or	0-1			control
		MFY	-	OFF, ON		Syntax: "MFY1" "MFY ON"
MODULATION						
BLOCK	none	NOM	÷.	-1	-	NO Modulation Syntax: "NOM"
	CH A	AEA	0-1	_	_	Channel A External
		or AEA	-	OFF, ON		Amplitude modulation Syntax: "AEA1" "AEA ON"
		AEP	0-1	_	_	Channel A External
		or AEP	-	OFF, ON		Phase modulation Syntax: "AEP1" "AEP ON"
		AIA	0-1	<u>-</u>	_	Channel A Internal
		or AIA	-	OFF, ON		Amplitude modulation Syntax: "AIA1" "AIA ON"
		AIP	0-1	<del>-</del>	_	Channel A Internal
		or AIP	-	OFF, ON		Phase modulation Syntax: "AIP1" "AIP ON"
		SPE	0-1	_	- <del>-</del>	Synchronous Phase
		or SPE	_	OFF, ON		modulation External Syntax: "SPE1" "SPE ON"
	CH B	BEA	0-1	_	<u>-</u>	Channel B External
		BEA	-	OFF, ON		Amplitude modulation Syntax: "BEA1" "BEA ON"
		BEP	0-1	_		Channel B External
		or BEP	-	OFF, ON		Phase modulation Syntax: "BEP1" "BEP ON"
				all pro-	3204	
STATUS BLOCK	CHAN	СНА	_	_	_ f .gu	select CHannel A Syntax: "CHA"
		СНВ	-	-		select CHannel B Syntax: "CHB"

	Front Panel Control	Mnemonic	Range	Suffix	Interrogation Response	Description Resolution Syntax
SWEEP BLOCK						
	CONT	SC	<del>a</del> sud	_		Sweep, Continuous Syntax: "SC"
	CNTR FREQ	CF	0-13 MHz	HZ, KHZ, MHZ	CF #########HZ or CF ##########HZ	Center Frequency Resolution: 1 μHz f < 100 kHz, 1 mHz f ≥ 100 kHz
					01	Syntax: "CF10KHZ"
	DISCRETE	SM	3	_	_	Sweep Mode -
		or SM	<u></u>	DSCR		DiSCRete Syntax: "SM3" "SM DSCR"
	MKR	MF	0-13 MHz	HZ, KHZ, MHZ	MF ########HZ	Marker Frequency Resolution:
	FREQ			MITZ	or MF #########HZ	1 $\mu$ Hz f < 100 kHz, 1 mHz f $\geq$ 100 kHz
						Syntax: "MF8.0MHZ
	MKR->	CFM	1500 0000		_	Center Frequency equals
	CF	18 <b>1</b>				Marker value Syntax: "CFM"
	RESET SWP	SRE				Sweep REset Syntax: "SRE"
	SINGLE	SS	9 <u></u> -			Sweep Single Syntax: "SS"
	SPAN	SPAN	0-13 MHz	HZ, KHZ, MHZ	SPAN#####.#####HZ or	SPAN
					SPAN##############	1 μHz f<100 kHz, 1 mHz f≥100 kHz
						Syntax: ''SPAN10MHZ''
	START FREQ	ST	0-13 MHz	HZ, KHZ, MHZ	ST ########HZ or ST #########HZ	STart frequency Resolution: 1 µHz f<100 kHz, 1 mHz f≥ 100 kHz Syntax:
						"ST3.567891KHZ"
	STOP	SP	0-13 MHz	HZ, KHZ, MHZ	SP #########HZ or SP #########HZ	StoP frequency Resolution: 1 μHz f<100 kHz,
						1 mHz f≥ 100 kHz Syntax: "SP7.1E6HZ
	TIME	STIM	5 ms-1000 s	SEC, MS	STIM ± #.####E ± ##SEC	Sweep TIMe Resolution: 1 MS Syntax: "STIM.3MS"
	TRIANGLE		1	-1	_	Sweep Mode -
		or SM		RAMP		Iinear RAMP Syntax: "SM1" "SM RAMP"
		SM	2	<u></u>	<u></u>	Sweep Mode -
		or SM		TRGL		linear TRianGLe Syntax: ''SM2''
				9		"SM TRGL"

the Status Byte is true (one), an SRQ has ocurred. See Service Request for the conditions causing a Service request. All other bits (0-5,7) indicate the present status of the noted function. The bits are true (one) only if the associated function/condition is true.

## **STATUS BIT**

The 3326A does not respond to a Parallel Poll.

## **PASS CONTROL**

The 3326A does not have the ability to take or pass control.

## **ABORT**

The 3326A responds to the Abort message (Interface Clear - IFC true) by stopping all Listener or Talker functions.

## ADDRESS ASSIGNMENT INFORMATION

The 3326A basic address is factory preset to decimal 18. In the Local mode, this address can be changed from the front panel by pressing the SHIFT and then the LOCAL keys. The display will show the current address; a new one can be entered via the 3326A keyboard.

Table 2: Status Byte Description

		bers: B7 B6 B5 B4 B3 B2 B1 B0
BIT NUMBER B7	VALUE 128	<b>DESCRIPTION</b> POWER RESTORED. Set when power is restored to the HP 3326A after power is interrupted. Reset when the HP 3326A is preset or receives device clear, selected device clear, or RST command.
B6	64	REQUIRE SERVICE. Set when the HP 3326A requires service (sent a SRQ). Cleared along with the SRQ line when a serial poll is performed it is also cleared when the condition causing the SRQ is removed.
B5	32	ERROR. Set when either a program or hardware error condition exist for the HP 3326A. Reset when the HP 3326A is preset, or receives a devic clear command, selected device clear command, RST command, or whe the error register is read with the IERR or ERR? HP-IB command.
B4	16	<b>READY.</b> Set when the HP 3326A has executed the last HP-IB command and is ready for the next command. Reset when the HP 3326A receive a device dependent command, device clear command, selected device clear command, or trigger.
B3	8	HARDWARE ERROR. Set when the HP 3326A detects an internal failure Reset with an INSTR PRESET, device clear command, selected devic clear command, RST command, or when the error register is read wit the IERR or ERR? HP-IB command.
B2	4	SWEEP START/IN PROGRESS. Set when the HP 3326A starts a sweep Reset when the sweep is stopped (either by reaching the stop frequenc or aborted by a front panel or HP-IB command). It is also reset when the HP 3326A is preset or receives a device clear command, selected device clear command, or RST command.
B1	2	SWEEP STOPPED. Set when the HP 3326A ends a sweep normally Reset when the HP 3326A is preset or receives a device clear command selected device clear command, or RST command.
B0	1	PROGRAM ERROR. Set when the HP 3326A receives an invalidable HP-IB command (e.g. command syntax or incompatible command for mode selected). Reset when the HP 3326A is preset or receives a devict clear command, selected device clear command, or RST command.

The new address will remain until changed by the operator because of the 3326A's non-volatile memory. However, should battery power be interrupted, the address will default to the factory preset address of 18.

## INTERFACE FUNCTION CODES

SH1	Source Handshake—full capability
AH1	Acceptor Handshake—full capability

Table 3: Error Summary

T6	Basic Talker—Serial Poll capability; no talk only
L4	Basic Listener—Unaddressed if addressed to talk;
	no listen only
SR1	Service Request—full capability
RL1	Remote Local—complete capability
PP0	Parallel Poll—no capability
DC1	Device Clear—full capability
DT1	Device Trigger—full capability
CO	Controller—no capability
E1	Driver Flectronics open collector

ERROR#	FRONT PANEL	ERROR DESCRIPTION
10	ONITY	Warral LID ID goods syntoy
10	SNTX	Illegal HP-IB code syntax
11	RMOT	Front panel keypress in remote
12	LOCK	LOCAL key pressed in local lockout
20-29	RNGE	Entered parameter out of range
30	BFR	Channel B cannot track
		Channel A in Two-Tone/HV on
40-49	INTR	Cannot interrogate or display parameter
50	CNVT	Units conversion rounded to zero
60-69	SUFX	Illegal units terminator
70	INC	Entry increment value or terminator error
80	AMPL	Incompatible with amplitude
86	MODL	Incompatible with modulation
87	MODE	Incompatible with mode
88	FREQ	Incompatible with frequency
89	CMBR	Incompatible with combiner
90	SWFR	Start and stop frequencies equal
94	DUTY	Pulse duty cycle too narrow
95	SWFR	Illegal sweep frequencies for HV option
96	SWFR	Illegal sweep frequency for internal modulation
100	RATE	Illegal sweep rate
110-114	DSWP	Illegal discrete sweep due to mode or lack of elements
115	DSHV	Illegal discrete sweep frequency with HV option
116	DSML	Illegal discrete sweep frequency with modulation
117	DSMD	Mode changed after discrete frequency sweep elements entered
		Cannot clear Channel A Phase Offset
120	POF	
130-139	HV	Cannot program High Voltage option
140	CSUM	Checksum error indicates bad instrument state
150	ODDT	Requested state is incompatible
160	CRPT	Corrupted power-on state is preset
170	A OL	Channel A overloaded
171	BOL	Channel B overloaded
172	SYOL	Sync output overloaded
173	AVCO	Channel A VCO unlocked
174	BVCO	Channel B VCO unlocked
180	XREF	External Reference unlocked
190	MCAL	Internal AM or PM cal unsuccessful
191	PCAL	Phase cal unsuccessful
192	ACAL	Amplitude cal unsuccessful
193	OCAL	DC Offset cal unsuccessful
194	OCAL	Residual Offset cal unsuccessful
300-399	FAIL	Self Test Error Codes

MNEMONIC DESCRIPTION	
AC	*Calibrate
ACAL	Auto Calibration
AEA	A External AM modulation
AEP	A External PM modulation
AIA	A Internal AM modulation
AIP	A Internal PM modulation
AM	Amplitude
AP	*Assign Zero Phase
BEA	B External AM modulation
BEP	B External PM modulation
BUSM	Bus Mode
CAL	Calibrate
CF	Set Sweep Center Frequency
CFM	Marker to Center Frequency
CHA	Select Channel A
CHB	Select Channel B
CMB	Combiner Enable
CMD	Calibration Mode
COF	Clear Phase Offset (Ch B)
DB	*dBm
DBM	dBm
DBV	dBV
DC	DC Function Selection
DE	*Degrees
DEG	Degrees
DISP	Display Blank
DN	Step Down/Decrement Value
DRCL	Discrete Recall Element
DRST	Discrete Reset Elements
DSAV	Discrete Save Element
DSCR	Discrete Sweep Mode
DUTY	Duty Cycle
EINC	Set the entry-increment value
ER?	*Output Error Number
ERR?	Output Error Number
EXT	External Calibration mode
FCNA	Set Channel A Function
FCNB	Set Channel B Function
FR	Frequency
FU	*Function of selected channel
HV	*High Voltage of selected channel
HVA	High Voltage A
HVB	High Voltage B
HZ	Hz
ID?	Output Instrument ID
INT	Internal Calibration Mode
KH	*kHz
KHZ	kHz
LRN	Output Learn String
MA	*External AM of selected channel
MASK	Request Mask
MD	*Bus Mode
MF	Sweep Marker
MFY	Modify on/off
МН	*MHz

MNEMONIC DESCRIPTION		
MHZ	MHz	
ML	Modulation Level (% AM or PM	
	Deviation)	
MODE	Mode	
MP	*External PM of selected channel	
MR	*mV RMS	
MS	milliseconds	
MULT	Multiphase Calibration Mode	
MV	*milliVolts peak-to-peak	
NOM	No modulation	
OF	DC Offset	
OFF	Off	
ON	On	
PC	Percent	
PH	Phase	
PRG	Program Learn String	
PULS	Pulse Mode selection	
RAMP RCL	Ramp Sweep Mode	
RDY?	Recall Register Output Ready Status	
RE RE	* Recall Register	
REV?	Output Revision Number	
RST	Instrument Preset	
SAV	Save Register	
SC	Sweep Continuous	
SEC	Seconds	
SER?	Output Serial Number	
SIN	Sine Function selection	
SM	Sweep Mode	
SP	Sweep Stop Frequency	
SPAN	Set Sweep Span	
SPE	Synchronous Phase External	
	modulation	
SQR	Square Function selection	
SR	*Save Register	
SRE	Sweep Reset	
SS	Sweep Single	
ST	Sweep Start Frequency	
STC	External Triggered Continuous Sweep	
STIM	Sweep Time	
STS	External Triggered Single Sweep	
TDN TE	External Triggered Decrement Value *Self Test	
TI	*Sweep Time	
TOFF	External Trigger no action	
TRGL	Triangle Sweep Mode	
TST	Self Test	
TUP	External Triggered Increment Value	
TWOC	Two-Channel Mode selection	
TWOP	Two-Phase Mode selection	
TWOT	Two-Tone Mode selection	
UP	Step Up/Increment Value	
VO	Volts peak-to-peak	
VR	*V RMS	
VRMS	V RMS	
WAIT	Wait	
ZPH	Zero Phase	

<sup>\*</sup>This is an HP 3325A HP-IB mnemonic that is totally compatible and accepted by the 3326A. Some may require specifing which channel is to be affected by the command.